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| 3 | 896 | recent\$2 near2 (table or map or mapping) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/20 14:10 |
| 4 | 212 | (history near (table or map or mapping)) near5 (read or fetch\$3) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/20 14:10 |
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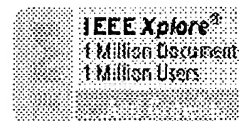
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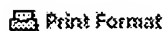
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1 Branch history guided instruction prefetching

Srinivasan, V.; Davidson, E.S.; Tyson, G.S.; Charney, M.J.; Puzak, T.R.;

High-Performance Computer Architecture, 2001. HPCA. The Seventh International Symposium on , 19-24 Jan. 2001

Pages:291 - 300

[\[Abstract\]](#) [\[PDF Full-Text \(848 KB\)\]](#) IEEE CNF

2 Neighborhood prefetching on multiprocessors using instruction history

Koppelman, D.M.;

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[\[Abstract\]](#) [\[PDF Full-Text \(1056 KB\)\]](#) IEEE CNF

3 A comparison of two strategies of dynamic data prefetching in software DSM

Haiming Liu; Weiwu Hu;

Parallel and Distributed Processing Symposium., Proceedings 15th

International , 23-27 April 2001

Pages:6 pp.

[\[Abstract\]](#) [\[PDF Full-Text \(124 KB\)\]](#) IEEE CNF

4 An adaptive network prefetch scheme

Jiang, Z.; Kleinrock, L.;

Selected Areas in Communications, IEEE Journal on , Volume: 16 , Issue: 3 , April 1998

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[\[Abstract\]](#) [\[PDF Full-Text \(340 KB\)\]](#) IEEE JNL

5 DRAM-page based prediction and prefetching

Haifeng Yu; Kedem, G.;

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6 Adaptive prefetching technique for shared virtual memory

Sang-Kwon Lee; Hee-Chul Yun; Joonwon Lee; Seungyoul Maeng;
Cluster Computing and the Grid, 2001. Proceedings. First IEEE/ACM International Symposium on , 15-18 May 2001
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7 Exploiting data mining techniques for broadcasting data in mobile computing environments

Saygin, Y.; Ulusoy, O.;
Knowledge and Data Engineering, IEEE Transactions on , Volume: 14 , Issue: 6 , Nov.-Dec. 2002
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[\[Abstract\]](#) [\[PDF Full-Text \(494 KB\)\]](#) IEEE JNL

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Bennett, J.E.; Flynn, M.J.;
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9 Design decisions influencing the UltraSPARC's instruction fetch architecture

Yung, R.;
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10 Branch-directed and stride-based data cache prefetching

Yue Liu; Kaeli, D.R.;
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11 A file-based adaptive prefetch caching design

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Computer Design: VLSI in Computers and Processors, 1990. ICCD '90. Proceedings., 1990 IEEE International Conference on , 17-19 Sept. 1990
Pages:463 - 466

[\[Abstract\]](#) [\[PDF Full-Text \(280 KB\)\]](#) IEEE CNF
